


For 10/784,493

First Named Inventor	Chistophe Chevallier	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
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Sheet 1 of 1		

U.S. Patent References				
Examiner Initials	Document/Patent No.	Issue/Publication Date	Name	Filing Date
W	5,777,922	July 7, 1998	Choi	October 18, 1996
W	5,825,782	10-20-1998	Roohparvar	01-22-1996
W	5,894,437	April 13, 1999	Chang	January 23, 1998
W	5,898,637	04-27-1999	Lakhani et al.	01-06-1997
W	5,996,106	11-30-1999	Seyyedy	02-04-1997
W	6,304,504	10-16-2001	Chevallier et al.	08-30-2000

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			
	WO	96/31882	Micron Technology, Inc.	10/10/96	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

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Examiner Signature	V. Nguyen	Date Considered	11/16/04
<p>*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>			